RBUS 1309-1

CERTIFICATE OF MAILING

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

h re Application of Inventor:

Craig E. Hampel et al.

Application No. 10/766,761

Filed: 28 January 2004

Title: Periodic Calibration for

Communication Channels by Drift

Tracking

Commissioner for Patents P.O. Box 1450

CUSTOMER NO. 38342

Group Art Unit: 2631 Examiner: Unknown

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.56

Sir:

It is requested that the information identified in this statement be considered by the Examiner and made of record in the above-identified application. This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56.

Enclosed with this statement is Form PTO-SB/08. The Examiner is requested to initial the forms and return them to the undersigned in accordance with M.P.E.P. 609.

Also enclosed with this statement is a copy of each cited document as required by 37 C.F.R. 1.98. The exception to this, in accordance with the 05 August 2003 Official Gazette Notice in which the USPTO waives the requirement for submitting copies of U.S. Patent and Publications for cases filed after 30 June 2003, is that copies of U.S. Patent documents and copies of U.S. Patent publications are not being submitted.

This statement should be considered under 37 C.F.R. 1.97(b) because it is being filed before the mailing date of the first Office Action on the merits.

Fee Authorization. The Commissioner is hereby authorized to charge underpayment of any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0869 (RBUS 1309-1). A copy of this authorization is enclosed.

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Respectfully submitted,

Mark A. Haynes, Reg. No. 30,846

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	10/766,761			
Filing Date	28 January 2004			
First Named Inventor	Craig E. Hampel			
Group Art Unit	2631			
Examiner Name				
Attorney Docket Number	RBUS 1309-1			

	U.S. PATENT DOCUMENTS							
Examiner Initials*	Cite No.¹	U.S. Patent Document Kind Code (if known)	or Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
	A50	5,548,146	Kuroda et al.	08-20-1996				
	A51	6,377,640	Trans	04-23-2002				
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Application Number 10/766,761

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First Named Inventor Craig E. Hampel

Group Art Unit 2631

Examiner Name

Attorney Docket Number RBUS 1309-1

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		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	C27	WIDMER, A.X., et al., "A DC-Balanced, Partitioned-Block, 8B/10B Transmission Code," IBM J. Res. Develop., Vol. 27, No. 5, September 1983, 440-451.	
	C28	BANU, MIHAI, et al., "TA 6.4: A 660Mv/s CMOS Clock Recovery Circuit with Instantaneous Locking for NRZ Data and Burst-Mode Transmission," IEEE International Solid State Circuits Conference, 1993, 102-103, 270.	
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	C30	CHANG, KEN K. Y., et al., "A 2 Gb/s Asymmetric Serial Link for High-Bandwidth Packet Switches," Hot Interconnects V, Stanford University, August 1997, 1-9.	
		HU, TIMOTHY H., et al., "A Monolithic 480 Mb/s Parallel AGC/Decision/Clock-Recovery Circuit in 1.2-• CMOS," IEEE Journal of Solid-State Circuits, Vol. 28, No. 12, December 1993, 1314-1320.	
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	C33	NAKAMURA, KAZUYUKI, et al., "A 6 Gbps CMOS Phase Detecting DEMUX Module Using Half-Frequency Clock," 1998 Symposium on VLSI Circuits Digest of Technical Papers, 196-197.	
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	C35	DALLY, W, et al., "DIGITAL SYSTEMS ENGINEERING," Cambridge University Press, 1998, pages 447-449.	
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